

**Notice of References Cited**Application/Control No.  
09/852,735Applicant(s)/Patent Under  
Reexamination  
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2836

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**U.S. PATENT DOCUMENTS**

*		Document Number Country Code-Number-Kind Code	Date MM-YYYY	Name	Classification
	A	US-5,623,387	04-1997	Li et al.	361/56
	B	US-5,774,3318	06-1998	McClure et al.	361/56
	C	US-5,043,782	08-1991	Avery	357/23.13
	D	US-5,272,371	12-1993	Bishop et al.	257/362
	E	US-5,821,797	10-1998	Kinugasa et al.	327/318
	F	US-5,539,327	07-1996	Shigehara et al.	326/30
	G	US-			
	H	US-			
	I	US-			
	J	US-			
	K	US-			
	L	US-			
	M	US-			

**FOREIGN PATENT DOCUMENTS**

*		Document Number Country Code-Number-Kind Code	Date MM-YYYY	Country	Name	Classification
	N					
	O					
	P					
	Q					
	R					
	S					
	T					

**NON-PATENT DOCUMENTS**

*		Include as applicable: Author, Title Date, Publisher, Edition or Volume, Pertinent Pages)
	U	Adel S. Sedra, Kenneth C. Smith, Microelectronic Circuits, Holt, Rinehart and Winston, 1987, pp. 408 - 410.
	V	
	W	
	X	

\*A copy of this reference is not being furnished with this Office action. (See MPEP § 707.05(a).)  
Dates in MM-YYYY format are publication dates. Classifications may be US or foreign.